

On page 2, in line 5, cancel “, or respectively,” substitute --or-- therefor;
in line 8, cancel “EP 0 777 180 A2” substitute --European Patent

Application No. 0 777 180-- therefor;

in line 15, cancel “NB.” substitute --Note:-- therefor;

5 in line 24, cancel “[sic]”.

On page 3, in line 3, insert a centered heading:

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--SUMMARY OF THE INVENTION--;

in line 5, cancel “accelerated significantly” substitute --significantly
accelerated-- therefor;

10 in line 7, after “achieved” insert --in accordance with the present
invention--;

in line 20, cancel “, or respectively,” substitute --or-- therefor;

in line 21, cancel “imulated/executed” substitute --simulated/executed--
therefor;

15 in line 22, cancel “partly in a simplified form [sic]” substitute --in a partly
simplified form-- therefor.

On page 4, in line 1, cancel “one” substitute --an-- therefor;

in line 3, insert the following:

-- In an embodiment, there is provided a method for system simulation with
20 simulated microcontrollers/microprocessors and appertaining peripheral
modules, said method comprising the steps of:

in a first sequence of steps, simulating said

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X 25 microcontroller/microprocessor and said peripheral modules with
predetermined signal patterns, said first sequence of steps having
markers inserted therein;

in a second sequence of steps, interrogating and evaluating system states
brought about by said simulation; and

interrupting said first sequence of steps for executing said second
sequence of steps as dictated by said markers that have been

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inserted into said first sequence, said second sequence of steps being executed in an accelerated operational mode that is adapted to said evaluation.--;

in line 4, after "achieved" insert --in accordance with the present
5 invention--;

in line 9, insert the following:

-- In an embodiment, there is provided a system for carrying out a method for system simulation with simulated microcontrollers/microprocessors and appertaining peripheral modules, said method comprising the steps of:

10 in a first sequence of steps, simulating said
microcontroller/microprocessor and said peripheral modules with
predetermined signal patterns, said first sequence of steps having
markers inserted therein;

15 in a second sequence of steps, interrogating and evaluating states of said
system brought about by said simulation; and
interrupting said first sequence of steps for executing said second
sequence of steps as dictated by said markers inserted into said
first sequence, said second sequence of steps being executed in an
accelerated operational mode that is adapted to said evaluation;

20 said system comprising:
a microprocessor control unit for simulating a module by generating
signal patterns with an essentially precise clock cycle and for
interrogating and evaluating states of said module that are
brought about by said simulation during a program interrupt by
activating an instruction set simulator.--;

25 in line 26, preceding "invention" insert --present--;

in line 28, cancel ", or respectively," substitute --or-- therefor.

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On page 5, in line 4, insert the following:

-- These and other features of the invention(s) will become clearer with
reference to the following detailed description of the presently preferred

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embodiments and accompanied drawings.

DESCRIPTION OF THE DRAWINGS

Figure 1 is a block circuit diagram of an arrangement for overall system simulation according to the present invention.

5 Figure 2 shows an excerpt of steps of a CPU that are operated in accordance with the present inventive method.

DETAILED DESCRIPTION OF THE PRESENTLY PREFERRED EMBODIMENTS--;

cancel line 7;

10 in line 29, cancel "or respectively," substitute --or-- therefor.

On page 6, cancel lines 8-15;

in line 18, preceding "invention" insert --present--;

in line 21, cancel "Pmn" substitute --PMn-- therefor;

in line 28, cancel "Pmn" substitute --PMn-- therefor.

15 On page 7, in line 14, cancel the comma;

in line 15, cancel "respectively,";

in line 21, preceding "inventive" insert --present--.

On page 8, in line 1, preceding "inventive" insert --present--;

in line 11, preceding "inventive" insert --present--;

20 below line 11, insert the following paragraph:

-- Although modifications and changes may be suggested by those of ordinary skill in the art, it is the intention of the inventors to embody within the patent warranted hereon all changes and modifications as reasonably and properly come within the scope of their contribution to the art.--